INFORMATION DISCLOSURE STATEMENT (IDS) BY APPLICANT

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First Named Inventor	Yong-Sung JEON et al.
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Examiner Name	SANDIFER, Matthew D.
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U.S. PATENT APPLICATION PUBLICATIONS

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FOREIGN PATENT DOCUMENTS

EXAMINER	FOREIGN PATENT			PUBL. DATE	NAME OF PATENTEE OR APPLICANT OF		LATION
INITIAL	DOCUMENT NUMBER			(YYYY-MM-DD)	THE CITED DOCUMENT		NO

NON-PATENT LITERATURE DOCUMENTS

EXAMINER INITIAL	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial symposium catalog, etc.) date, page(s), volume-issue number(s), publisher, city and/or country where published.	TRANSLATIO YES NO			
	VIKTOR FISCHER, et al; "True Random Number Generator Embedded in				
	Reconfigurable Hardware", Workshop on Cryptographic Hardware and				
	Embedded Systems, Ches 2002; Lecture Notes on Computer Science, Springer				
	Verlag, Vol. 2523, 13 August 2002, pages 415-430; XP001160534 San				
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	DRIES SCHELLEKENS, et al; "FPGA Vendor Agnostic True Random Number Generator", Field Programmable Logic and Applications, 2006, FPL '06.				
	International Conference on, IEEE, PI, 28 August 2006, pages 1-6,				
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	pages 203-230, XP002582230 AT&T Bell Laboratories, Morristown, New Jersey 07960, USA the whole document				
	European Search Report: EP 04 77 4229				
EXAMINER	DATE CONSIDERED				

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